Microcomputers

■8-Bit Single Chip Microcomputers, MN1880 Family (continued)

Category	Type No.	ROM	RAM (×8-bit)	Input/ Output (Lines)	Speed	Supply Voltage	Package		Remarks	
	Type No.	(K Byte)			(μs)	(typ) (V)	1 ackage	No.	Hemains	
	▲MN18P82410(A)	24	580	65	0.5	4.5 to 5.5	QFH080-P-1212	L63	One time version	
	MN1883210 [5V version]	32	528	69	0.5 122	4.5 to 5.5 3.0 to 5.5	QFP084-P-1818 L72		A/D input	
	MN1883210 [(A)3.3V version]	32	688	69	0.5 122	3.3 to 5.5 3.0 to 5.5	QFH084-P-1414			
	MN18P83210	32	656	69	0.5 122	4.5 to 5.5	QFP084-P-1818	L72	One time version	
A (D: 4 Times	MN1882414	24	448	70	0.5 122	4.5 to 5.5 3.0 to 5.5	QFP084-P-1818	L72	A/D input	
A/D input Timer upgraded	MN1883214	32	928	70	0.5 122	4.5 to 5.5 3.0 to 5.5	QFP084-P-1818	L72	A/D input	
	MN1883220	32	2592	67	0.5 122	4.5 to 5.5 3.0 to 5.5	QFH080-P-1414	L64	A/D input	
	MN1884820	48	2720	67	0.5 122	4.5 to 5.5 3.0 to 5.5	QFH080-P-1414	L64	A/D input	
	MN18P83220	32	2592	67	0.5 122	4.5 to 5.5 3.0 to 5.5	QFH080-P-1414	L64	One time version	
	▲MN18P83214	32	928	71	0.5 122	4.5 to 5.5	QFP084-P-1818	L72	One time version	
	MN1882421	24	800	72	0.33 122	2.7 to 4.0	QFH080-P-1212	L63	A/D input	
	▲MN18P83221	32	928	66	0.4	2.7 to 5.5	QFH080-P-1212	L63	One time version	
A/D input	MN1880023	64KB ext.	704	73	0.2	4.5 to 5.5	QFP084-P-1818E	L73	A/D input	
Timer upgraded	MN1880024	64KB ext.	928	73	0.2	4.5 to 5.5	QFP064-P-1818E	L73	A/D input	
High-speed bus expanded	▲MN1884824	48	928	73	0.2	4.5 to 5.5	QFP084-P-1818E	L73	A/D input	
	▲MN18P86424	64	928	73	0.2	4.5 to 5.5	QFP084-P-1818E	L73	One time version	
Memory space	MN18806	2MB ext.	2MB ext.	63	0.5 0.4	4.5 to 5.5 4.75 to 5.5	QFP100-P-1818	L76		
expanded	MN18P86420	64	2720	67	0.5 122	4.5 to 5.5 3.0 to 5.5	QFH080-P-1414	L64	One time version	

[▲]Under development

■8-Bit Single Chip Microcomputers, MN1860 Family

Category	Type No.	ROM (K Byte)	RAM (×8-bit)	Input/ Output (Lines)	Speed (μ s)	Supply Voltage (V)	Package	No.	Remarks
					0.20	4.5 to 5.5	0.777.00 P.1010	1	
A/D	MN1860003	Ext.	1088	85	122	4.5 to 5.5	QFP100-P-1818	L76	_
A/D High -speed bus expanded	MN1860004	Ext.	1088	85	0.16 122	4.5 to 5.5 4.5 to 5.5	QFH100-P-1414	L75	
	MN1866405	64	1856	85	0.20 122	3.0 to 5.5 3.0 to 5.5	QFH100-P-1414	L75	
	MN1866406	64	1856	85	0.16 0.25	4.5 to 5.5 3.0 to 5.5	QFH100-P-1414 QFP100-P-1818	L75 L76	
	MN18P66405	64	1856	85	0.25 122	3.3 to 5.5 3.3 to 5.5	QFH100-P-1414 QFP100-P-1818	L75 L76	

■8-Bit Single Chip Microcomputers, MN10100 Family

Category	Type No.	ROM (K Byte)	RAM (×8-bit)	Input/ Output (Lines)	Speed (μs)	Supply Voltage (V)	Package		Remarks
								No.	
A/D	▲ MN101C01D	64*1	2K*1	80*1	0.1	4.5 to 5.5 7.0 to 5.5	QFS080-P-1414	L68	
	▲MN101C01C	48*1	2K*1						
	OMN101C01A	32 ^{*1}	1.5K ^{*1}						
	▲MN101C027	16*1	0.5K*1	64*1	0.1 122	4.5 to 5.5 2.0 to 5.5	QFP064-P-1414	L60	
	▲MN101C025	8*1	0.5 K *1						
A/D, LCD	▲MN101C03A	32*1	1.5K*1	100*1	0.1	4.5 to 5.5 2.0 to 5.5	QFP100-P-1818	L76	
	▲MN101C037	16*1	1K*1		122			Lio	

▲Under development *1: at a single chip mode \bigcirc Under planning (Package Symbol) QFH = \underline{Q} uad \underline{F} lat \underline{H} igh Package, QFP = \underline{Q} uad \underline{F} lat \underline{P} ackage, QFS = \underline{Q} uad \underline{F} lat L-leaded \underline{S} mall Package